



Features

- ◇ SAW Delay-Line
- ◇ Single-Ended Operation
- ◇ DIP Package
- ◇ Electrostatics Sensitive Device (ESD)

Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	193.5	195	196.5
Insertion Loss	dB	-	20	26
3 dB Bandwidth	MHz	25	29.21	-
30 dB Bandwidth	MHz	-	43.25	45
Passband Variation($f_0 \pm 12.5\text{MHz}$)	dB	-	0.7	1.5
Group Delay Variation($f_0 \pm 12.5\text{MHz}$)	nsec	-	260	300
Absolute Delay	usec	9.95	10	10.05
Ultimate Rejection	dB	30	34	-
Material Temperature coefficient	KHz/°C	-18.33		
Substrate Material	-	YZ LN		
Ambient Temperature	°C	25		
Operating Temperature Range	°C	-40	-	+85
Storage Temperature Range	°C	-45	-	+105
DC Voltage	V	0		
Input Power	dBm	-	-	10
ESD Class	-	1		
Package Size	DIP7320 (73.28x19.94x6.5mm ³)			

Notes:

1. All specifications are based on the test circuit shown;
2. In production, all specifications are measured by Agilent Network analyzer and full 2 port calibration at room temperature;
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances;
4. This is the optimum impedance in order to achieve the performance show.

	SIPAT Co., Ltd. (CETC No.26 Research Institute) #14 Nanping Huayuan Road, Chongqing, China, 400060	Part Number	DLN19501	
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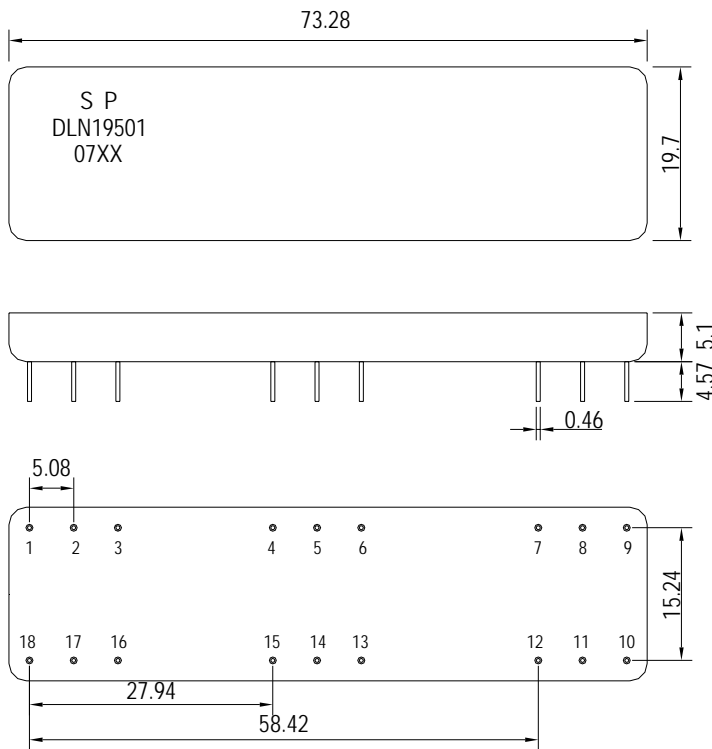
Matching Configuration



Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

Package Dimension



Pad Configuration:

- Input 4
- Output 11
- Ground All Others

Marking Configuration:

- 1) SP: Manufacturer Name
- 2) DLN19501: Part Number
- 3) 07XX: Date Code

Package: DIP7320

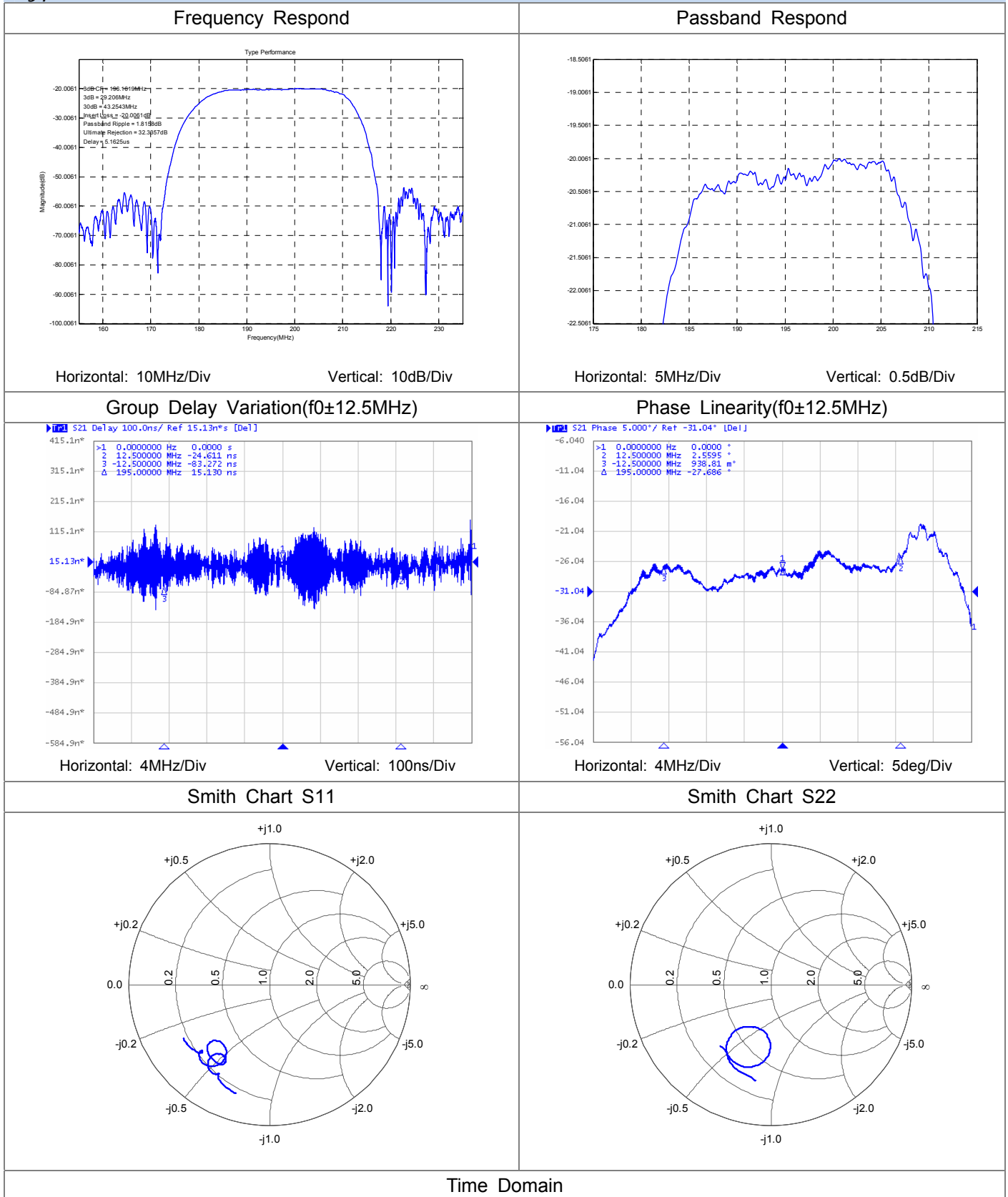
Unit: mm



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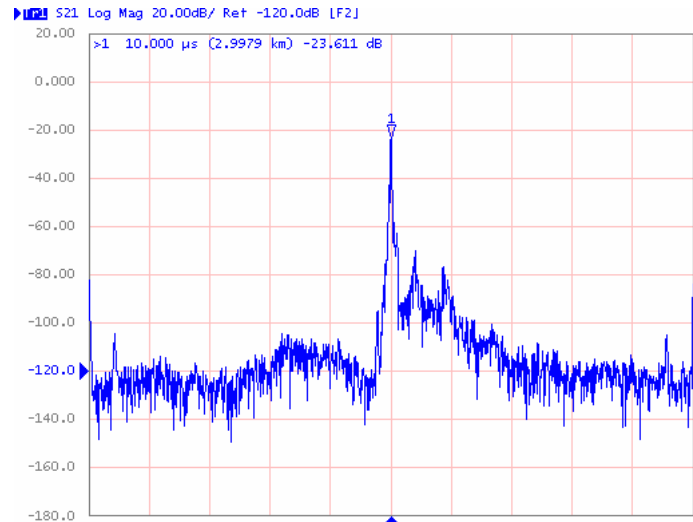
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Typical Performance



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Horizontal: 2μS/Div Vertical: 20dB/Div



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