



### Features

- ◇ For IF SAW filter
- ◇ Single-ended operation
- ◇ Ceramic Surface Mount Package
- ◇ Small size
- ◇ RoHS compliant (2002/95/EC), Pb-free

### Specifications

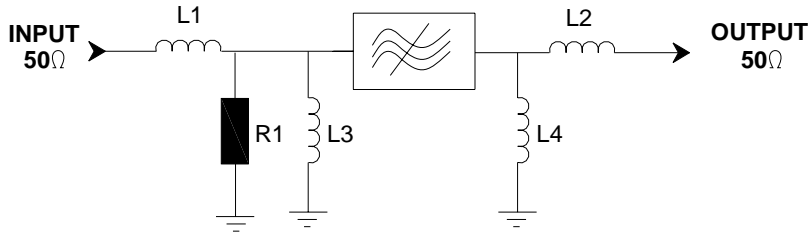
| Parameter                                       | Unit        | Minimum | Typical | Maximum |
|---|-------------|---------|---------|---------|
| Center Frequency                                | MHz         | 139.75  | 140     | 140.25  |
| Insertion Loss                                  | dB          | -       | 13      | 15      |
| 1 dB Bandwidth                                  | MHz         | 31      | 32.06   | -       |
| 3 dB Bandwidth                                  | MHz         | 32      | 33.54   | -       |
| 40 dB Bandwidth                                 | MHz         | -       | 37.54   | 40      |
| Phase Linearity( $f_0 \pm 13\text{MHz}$ )       | deg         | -       | 5       | 6       |
| Group delay variation( $f_0 \pm 13\text{MHz}$ ) | nsec        | -       | 85      | 100     |
| Passband Variation( $f_0 \pm 15\text{MHz}$ )    | dB          | -       | 0.5     | 1       |
| Absolute Delay                                  | usec        | -       | 0.96    | -       |
| Ultimate Rejection( $f_0 \pm 20\text{MHz}$ )    | dB          | 38      | 39      | -       |
| Material Temperature coefficient                | KHz/°C      | -13.2   |         |         |
| Substrate Material                              | -           | YZ LN   |         |         |
| Ambient Temperature                             | °C          | 25      |         |         |
| DC Voltage                                      | V           | 0       |         |         |
| Input Power                                     | dBm         | -       | -       | 10      |
| ESD Class                                       | -           | 1A      |         |         |
| Package Size                                    | SMD13.3*6.5 |         |         |         |

#### Notes:

1. All specifications are based on the test circuit shown;
2. In production, all specifications are measured by Agilent Network analyzer and full 2 port calibration at room temperature;
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances;
4. This is the optimum impedance in order to achieve the performance show.

|  |  |             |            |          |
|--|--|-------------|------------|----------|
|  | <b>SIPAT Co., Ltd.</b><br>( CETC No.26 Research Institute )<br>#14 Nanping Huayuan Road,<br>Chongqing, China, 400060 | Part Number | LB140DS21  |          |
|  |  | Rev. Date   | 2010-01-14 |          |
|  |  | Ver.        | 2.0        | Page 1/3 |

### Matching Configuration

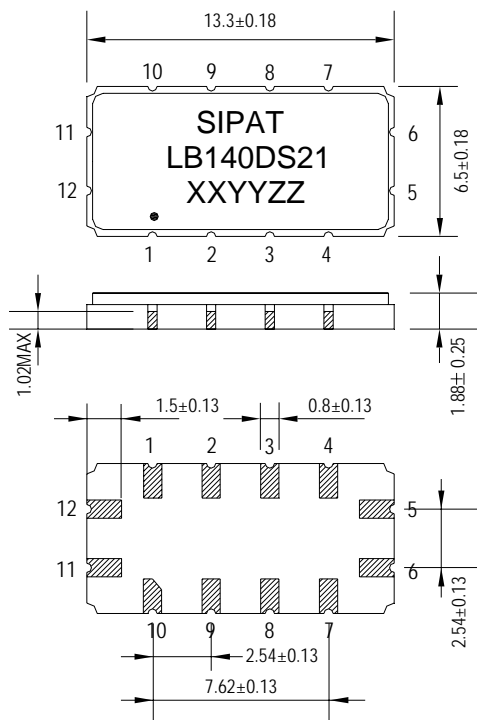


$L1=L2=82\text{nH}$   $L3=150\text{nH}$   $L4=120\text{nH}$   
 $R1=510\Omega$

Source/Load Impedance=50 ohm

Notes - Component values may change depending  
on board layout.

### Package Dimension



#### Pad Configuration:

Input: 11

Output: 5

Ground: All Others

#### Marking Configuration:

- 1) •: Pad Number 1 Index
- 2) SIPAT: Manufacturer Name
- 3) LB140DS21: Part Number
- 4) XXYY: Date(Year/month)
- 5) ZZ: Identified Code

Package: SMD13.3\*6.5

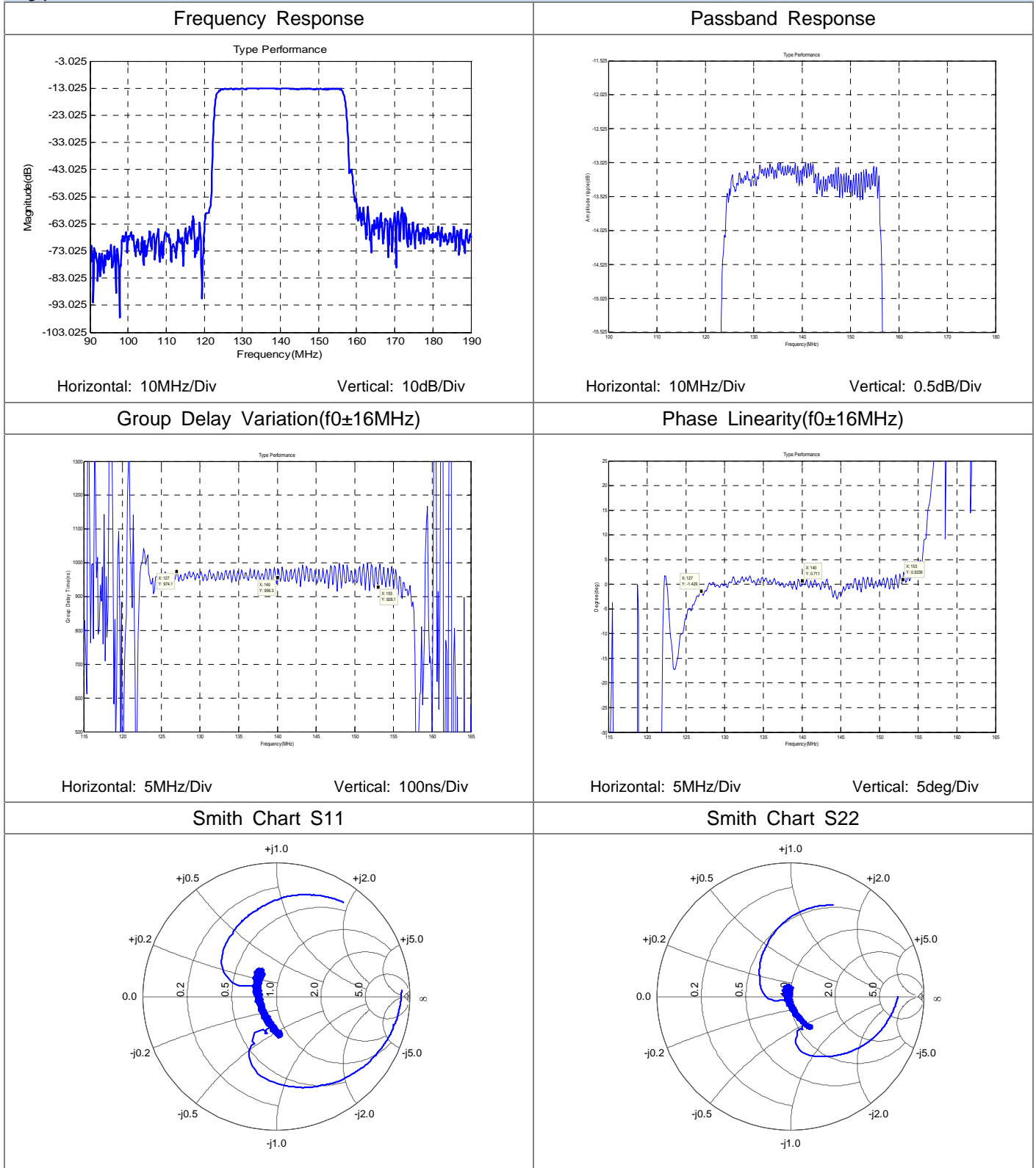
Unit: mm



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Typical Performance



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