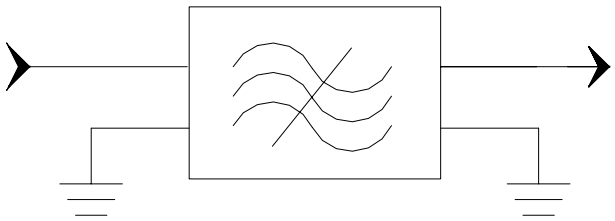
## Specifications

| Parameter | Unit | Minimum | Typical | Maximum |
|---|---|---|---|---|
| *Center Frequency* | MHz | 114.9 | 115 | 115.1 |
| *Insertion Loss* | dB | | 24 | 25 |
| *2 dB Bandwidth* | MHz | 10 | 10.04 | |
| *35 dB Bandwidth* | MHz | | 10.72 | 10.8 |
| *40 dB Bandwidth* | MHz | | 10.77 | 11.2 |
| *45 dB Bandwidth* | MHz | | 10.81 | 12 |
| *Passband Variation* | dB | | 1 | 1.5 |
| *Absolute Delay* | usec | | 3.84 | 4 |
| *Ultimate Rejection* | dB | 43 | 48 | |
| *Subatrate Material* | | YZ | | |
| *Ambient Temperature* | °C | 25 | | |
| *Package Size* | DIP3512 (35.0x12.8x4.7mm$^3$) | | | |

**Notes:**

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

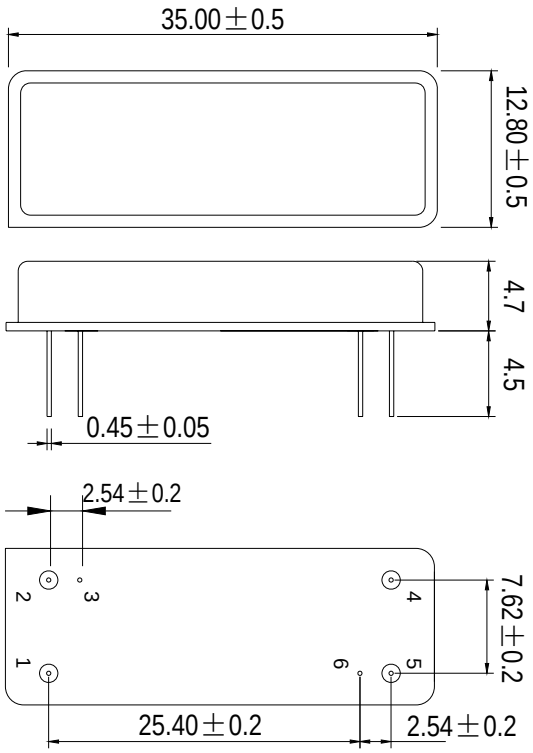
## Matching Configuration



**Source/Load Impedance=50 ohm**

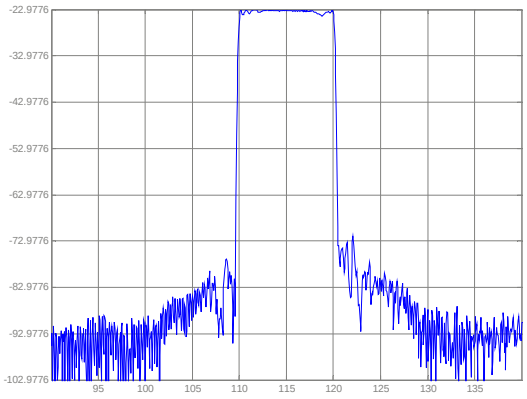
Notes - Component values may change depending on board layout.

## Package Dimension

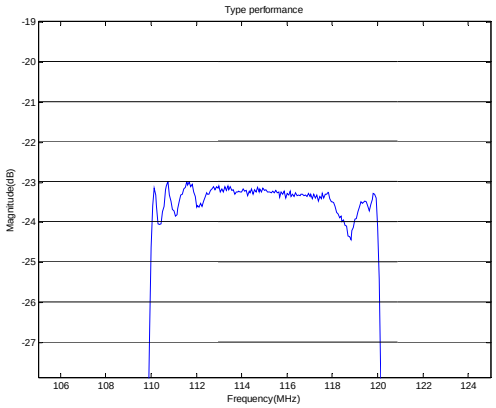


## *Typical Performance*

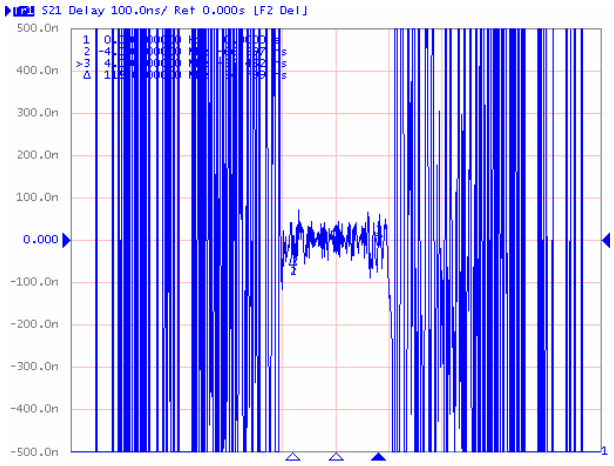
### Frequency Respond



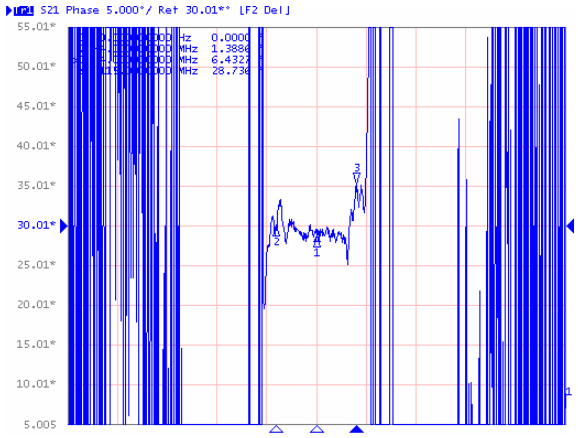

### Passband Respond



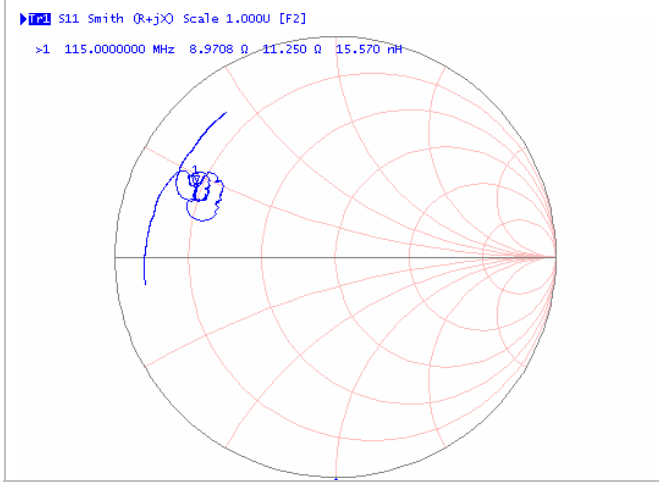

### Group Delay Variation(f0±4MHz)




### Phase Linearity(f0±4MHz)




### Simth Chart S11




### Simth Chart S22