

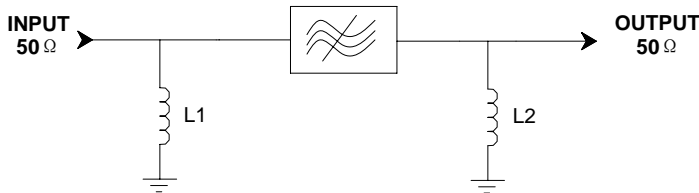
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.875	140	140.125
Insertion Loss	dB	-	30	31
3 dB Bandwidth	MHz	35	35.1	-
20 dB Bandwidth	MHz	-	35.77	35.85
25 dB Bandwidth	MHz	-	35.86	36
35 dB Bandwidth	MHz	-	36.5	36.8
Absolute Delay	usec	-	3	3.5
Passband Variation	dB	-	0.8	1.5
Ultimate Rejection	dB	45	50	-
Substrate Material		YZLN		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm ³)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



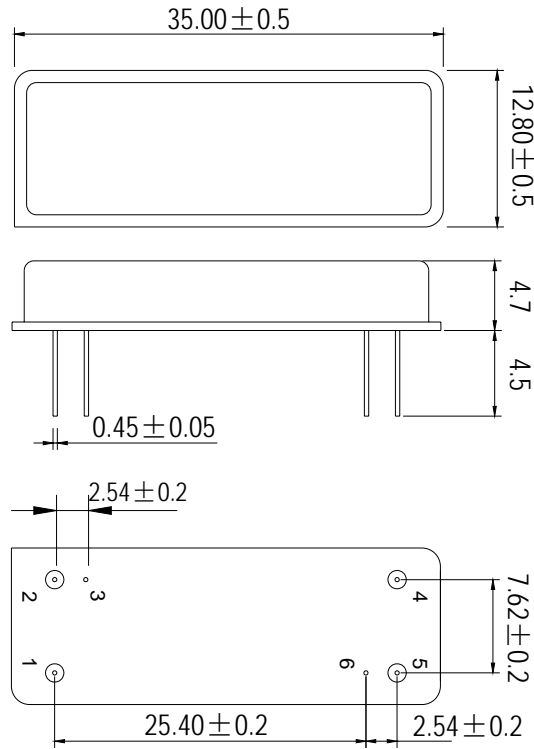
$L1=L2=82nH$

Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	SP LBN14060	
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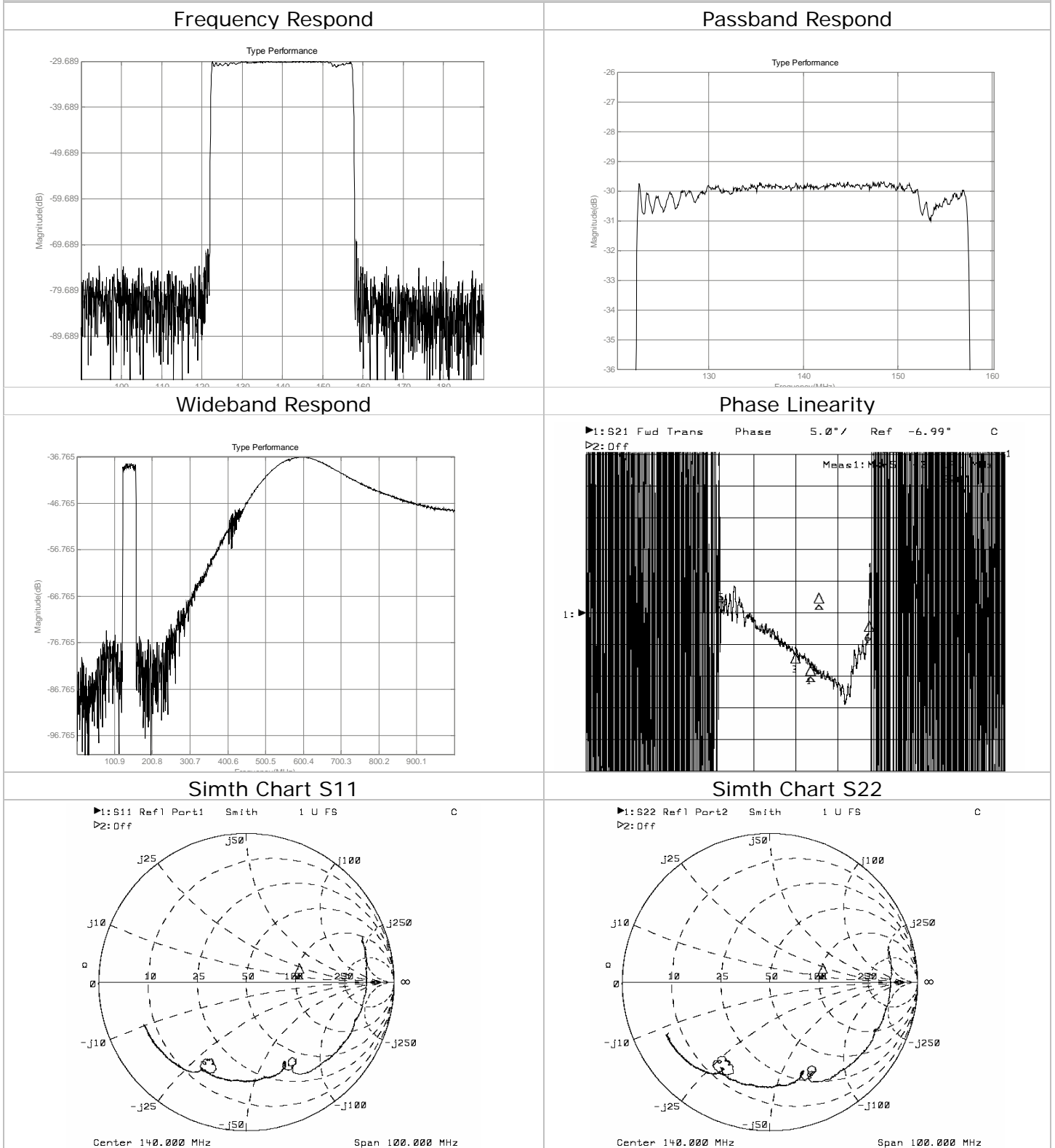
Package Dimension



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Typical Performance




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Marking

注意：如果把匹配图中的两个电感的值都换成47nH，则插损将降为27dB左右，但波纹将增加到2dB左右。

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