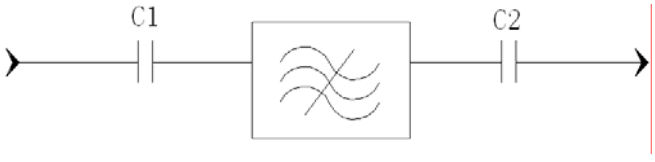
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.9	140	140.1
Insertion Loss	dB	-	20	21.5
1 dB Bandwidth	MHz	9	9.3	-
3 dB Bandwidth	MHz	10	10.05	-
40 dB Bandwidth	MHz	-	12.85	13.2
Passband Variation	dB	-	0.9	1.2
Absolute Delay	usec	-	1.3	-
Ultimate Rejection	dB	40	42	-
Material temperature coefficient	KHz/°C	-11.5		
Ambient Temperature	°C	25		
Package Size	SMP 53C			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

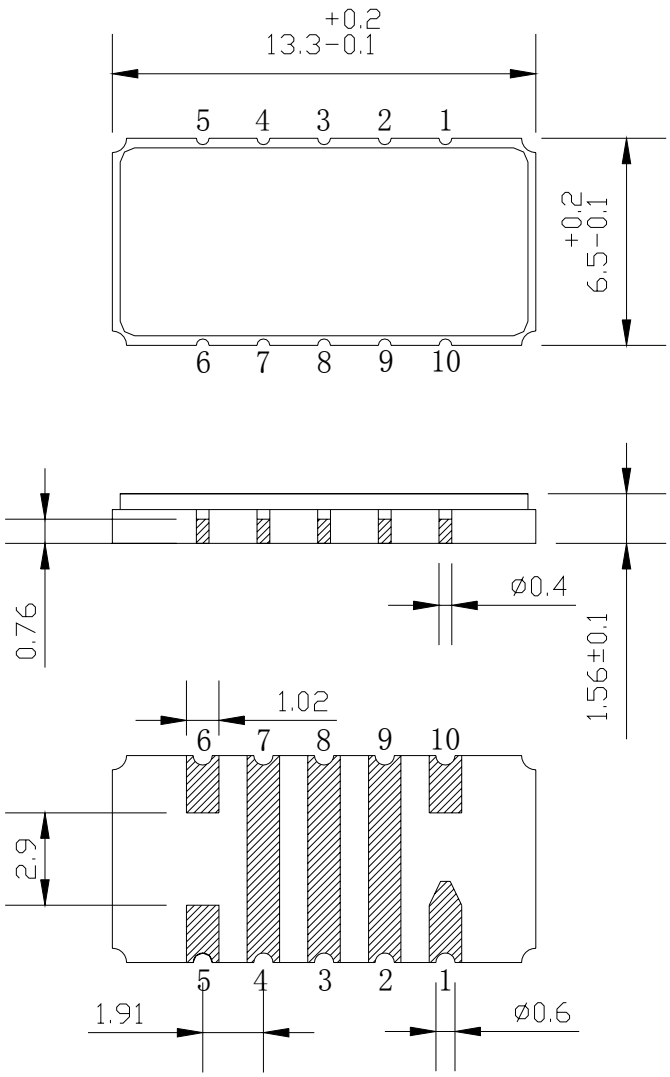


C1= 36pF C2= 36pF

Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

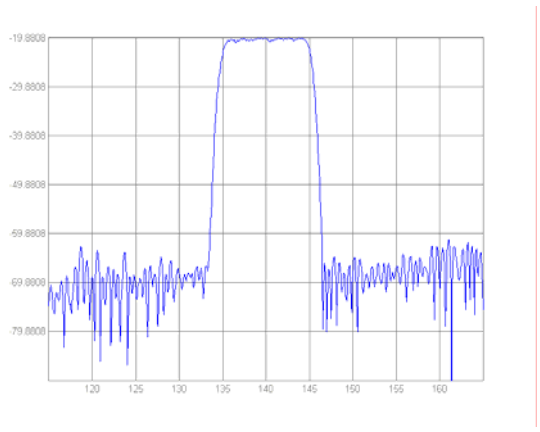
Package Dimension



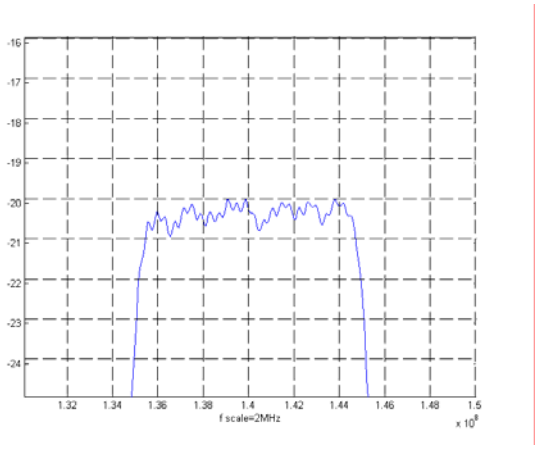
Pin 10: input
Pin 5:output

Typical Performance

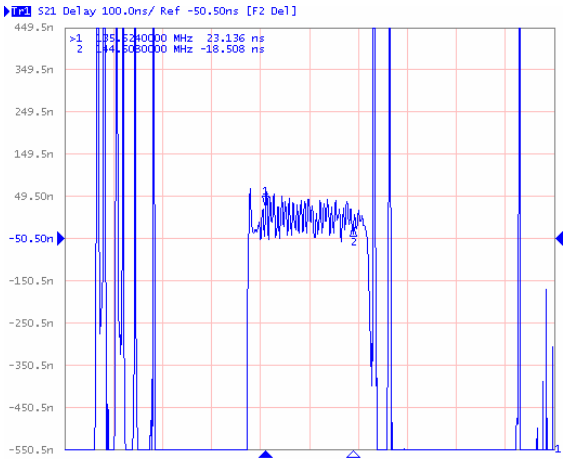
Frequency Respond



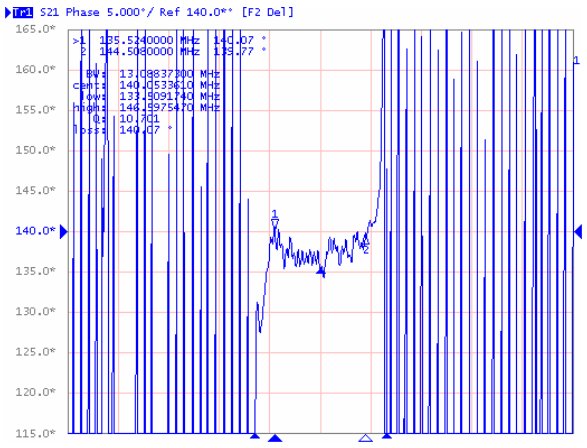
Passband Respond



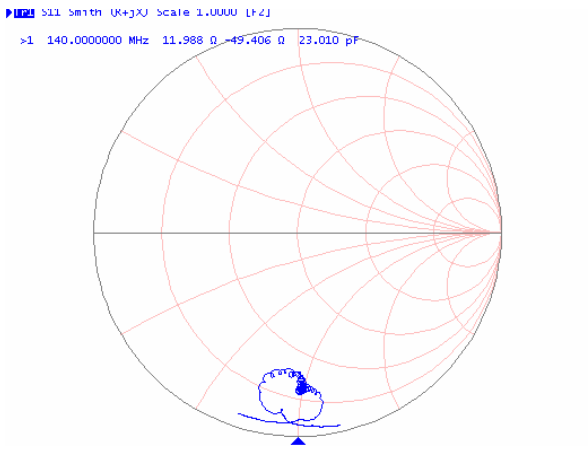
Group Delay(135.524~144.508MHz)



Phase Linearity(135.524~144.508MHz)



Simth Chart S11



Simth Chart S22