

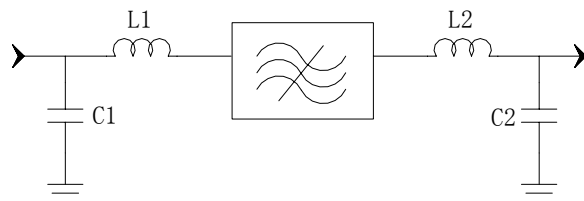
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	113.960	114	114.040
Insertion Loss	dB	-	8.7	10
0.75 dB Bandwidth	MHz	0.48	0.52	-
20 dB Bandwidth	MHz	-	1.68	1.72
40 dB Bandwidth	MHz	-	2.1	2.5
Passband Variation	dB	-	0.2	0.75
Absolute Delay	usec	-	1.75	-
Phase Linearity( $f_0 \pm 0.225\text{MHz}$ )	deg	-	2	5
Group Delay Variation				
$f_0 \pm 0.15\text{MHz}$	nsec		50	100
$f_0 \pm 0.25\text{MHz}$			75	200
Ultimate Rejection				
$f_0 \pm 3\text{MHz}$	dB	42	45	
Material temperature coefficient	KHz/°C	1x $F_0$ /1000		
Ambient Temperature	°C	25		
Package Size	SMD 13.3*6.5			

#### Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration

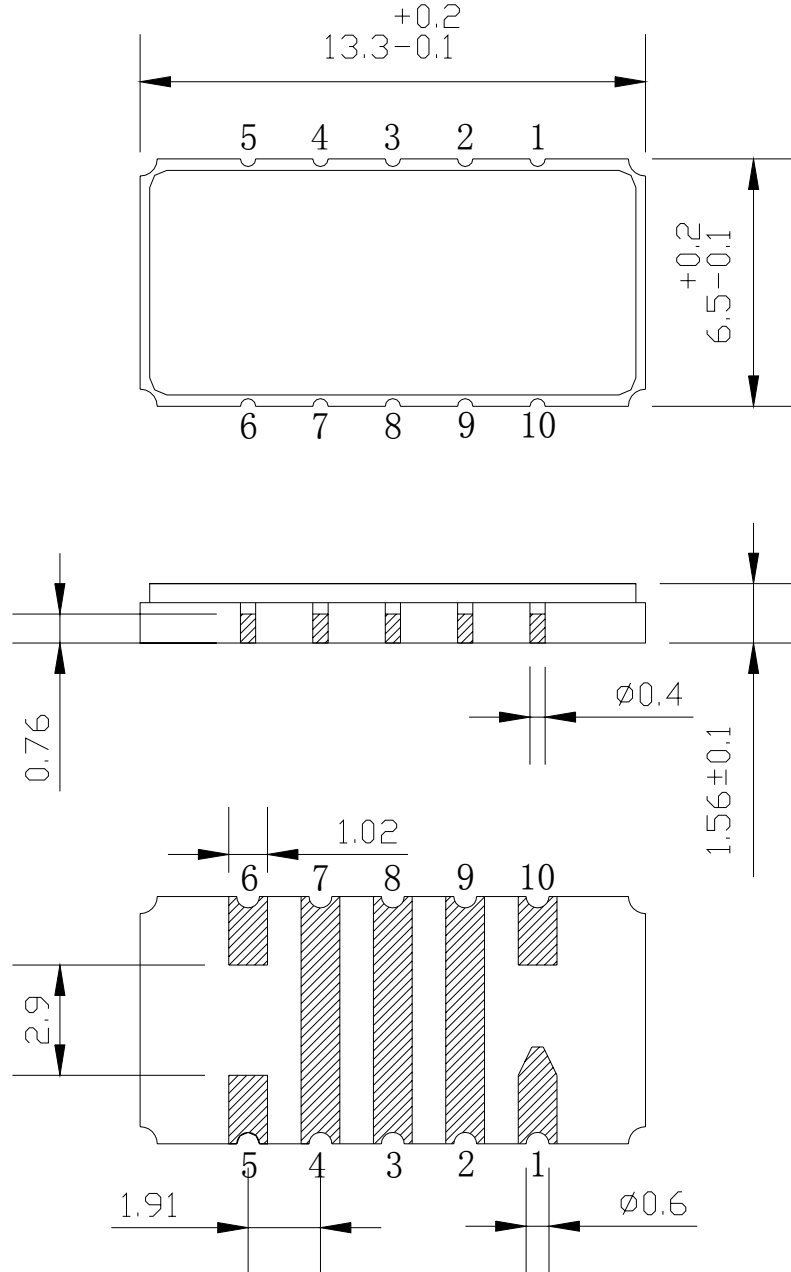


**L1=120+18nH    L2=180nH**  
**C1=56pF        C2=56pF**  
**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

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Package Dimension



**Input:10**  
**Output:5**

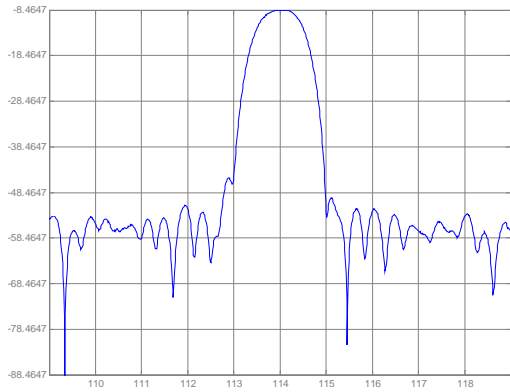


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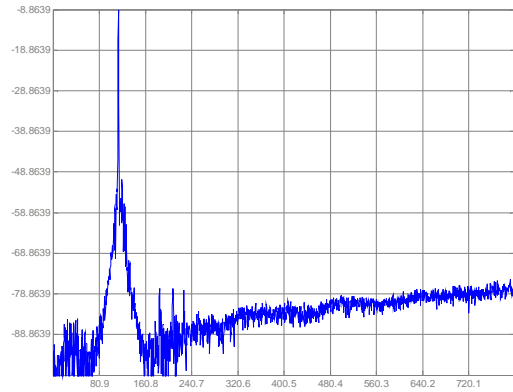
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Typical Performance

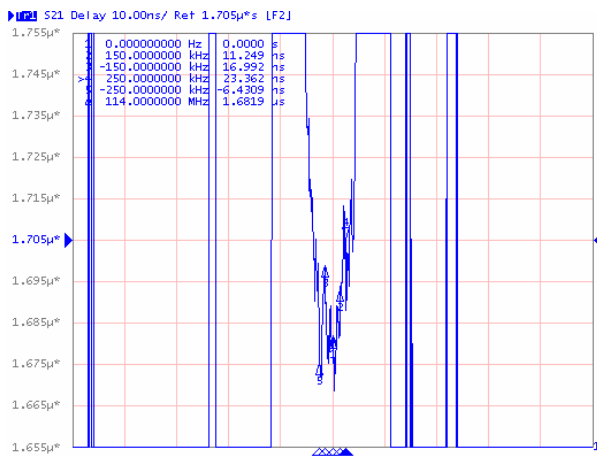
Frequency Respond



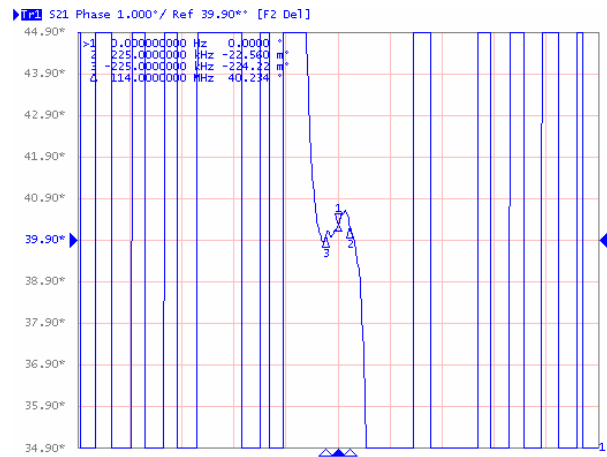
Wideband Respond



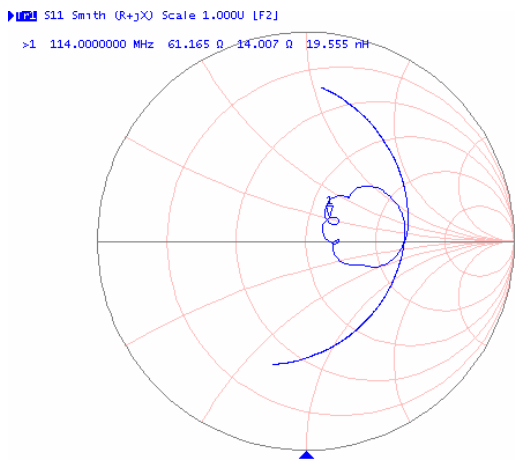
Group delay variation



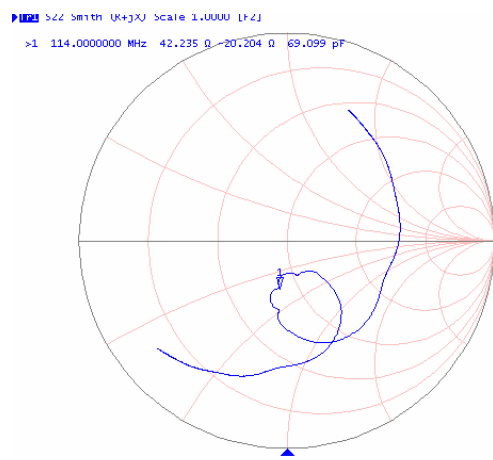
Phase Linearity(f0±225k)



Smith Chart S11



Smith Chart S22



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