

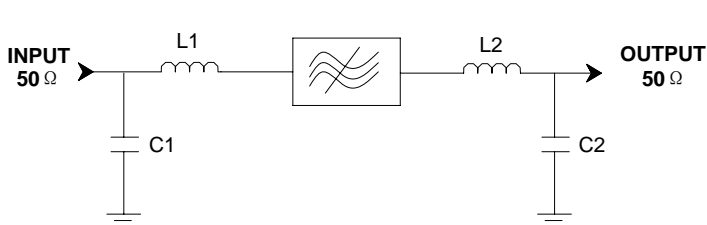
Specifications

Parameter	Unit	Minimum	Typical	Maximum	
Center Frequency	MHz	114.9	115	115.1	
Insertion Loss	dB	-	27.4	28.5	
3 dB Bandwidth	MHz	4.75	4.79	4.85	
Passband Variation	dB	-	0.8	1.5	
Absolute Delay	usec	-	3.84	-	
Group Delay Variation($f_0 \pm 2.375\text{MHz}$)	nsec	-	90	-	
Ultimate Rejection	$f_0 \pm 2.8\text{MHz}$	dB	35	47	-
	$f_0 \pm 3\text{MHz}$	dB	40	52	-
	$f_0 \pm 3.4\text{MHz}$	dB	50	55	-
	$f_0 \pm 7.4\text{MHz}$	dB	55	56	-
	$f_0 \pm 15\text{MHz}$	dB	50	64	-
Material Temperature coefficient	KHz/°C	0.115			
Ambient Temperature	°C	25			
Package Size	DIP3512 (35.0x12.8x4.7mm ³)				

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=180nH L2=(100+10)nH
C1=47pF C2=56pF
Source/Load Impedance=50 ohm

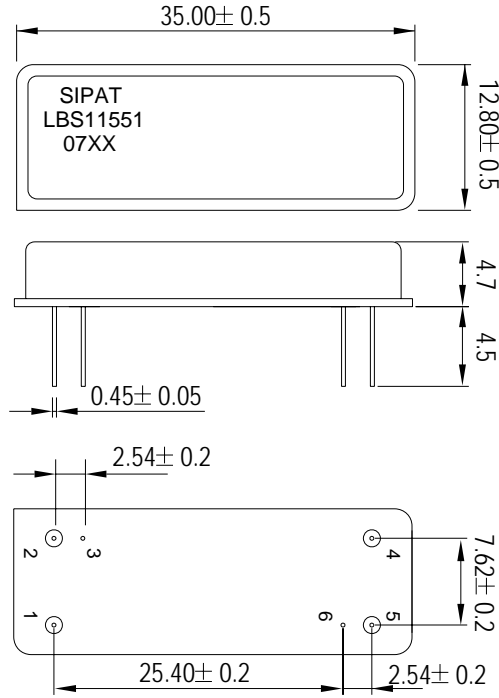
Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
(CETC No.26 Research Institute)
#14 Nanping Huayuan Road,
Chongqing, China, 400060

Part Number	LBS11551	
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Package Dimension



- 1) ∴ Pad Number 1 Index
- 2) SIPAT: Manufacturer name
- 3) LBS11551: Part Number
- 4) 07XX: Date Code

Input	1
Output	5
Ground	All Others

Package: DIP3512

Unit: mm

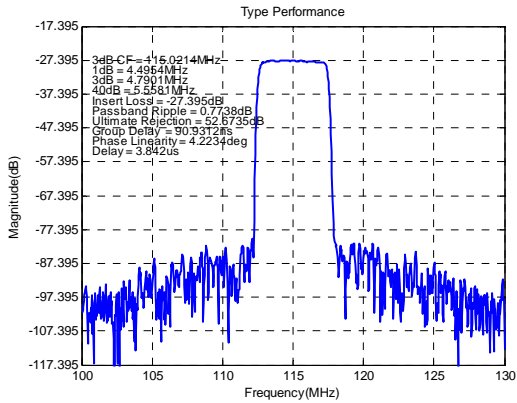


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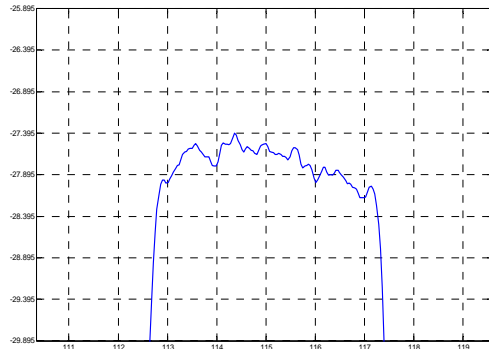
Typical Performance

Frequency Respond



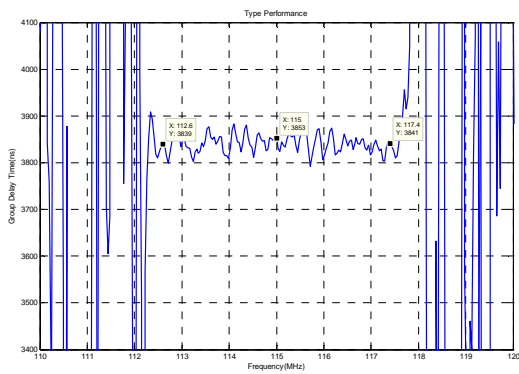
Horizontal: 5MHz/Div Vertical: 10dB/Div

Passband Respond



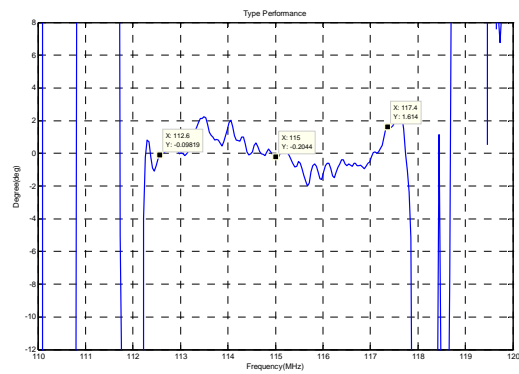
Horizontal: 1MHz/Div Vertical: 0.5dB/Div

Group Delay Variation (f0±2.4MHz)



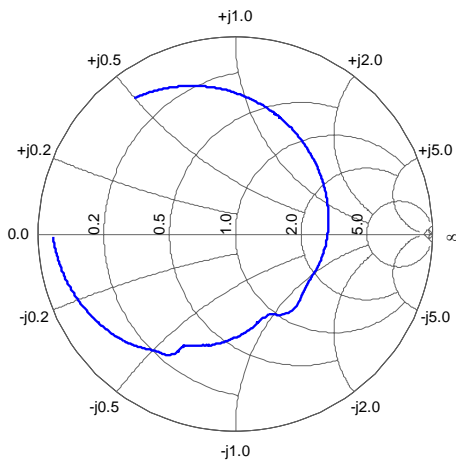
Horizontal: 1MHz/Div Vertical: 100ns/Div

Phase Linearity (f0±2.4MHz)

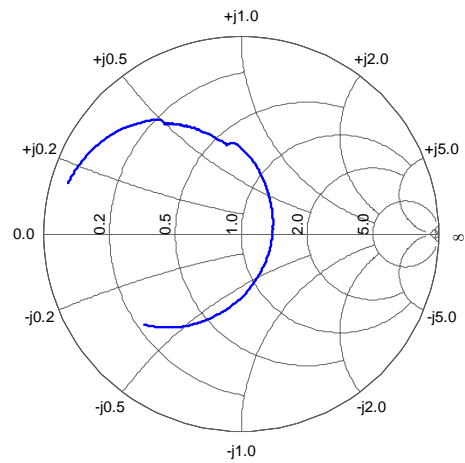


Horizontal: 1MHz/Div Vertical: 2deg/Div

Smith Chart S11



Smith Chart S22




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Maximum Ratings

Operating Temperature Range	°C	[-40°C;+85°C]
Storage Temperature Range	°C	[-40°C;+105°C]
DC Voltage	V	0
Input Power	dBm	10 max
ESD Class		1

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