

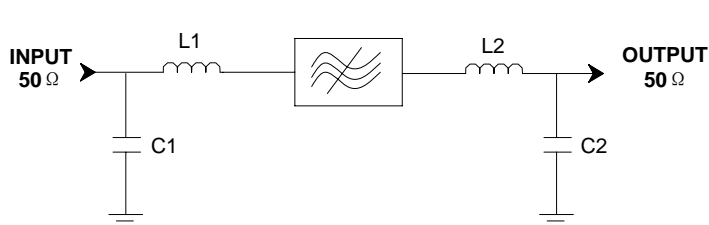
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	159.92	160	160.08
Insertion Loss	dB	-	22	25
1dB Bandwidth	MHz	4.1	4.2	-
3 dB Bandwidth	MHz	4.4	4.46	-
40 dB Bandwidth	MHz	-	5.35	5.4
50 dB Bandwidth	MHz	-	6.12	6.5
Passband Variation	dB	-	0.6	1
Absolute Delay	usec	-	2.76	3.75
Phase Linearity(over 1dB bandwidth)	deg	-	5	6
Group Delay Variation(over 1dB bandwidth)	nsec	-	120	150
Ultimate Rejection($f_0 \pm 3.2\text{MHz}$)	dB	45	47	-
Material Temperature coefficient	KHz/°C	0.16		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.0x12.8x4.7mm3)			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

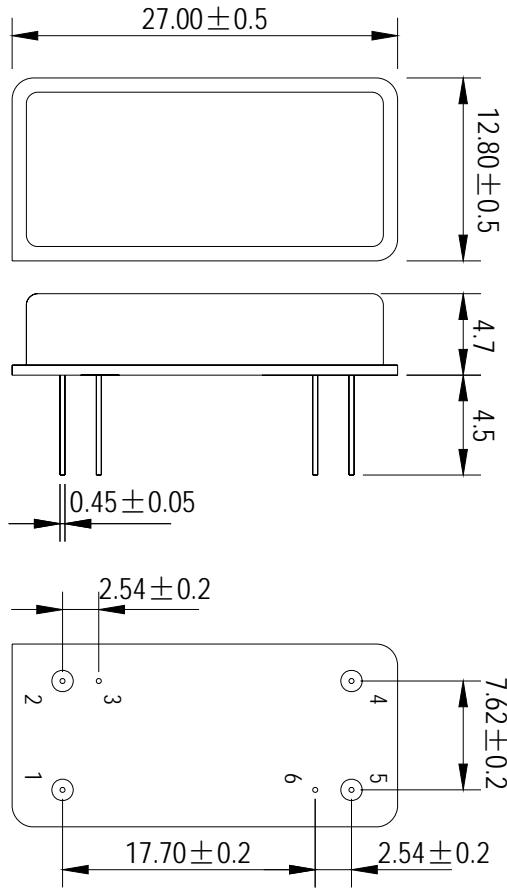


L1=100nH L2=82nH
C1=C2=47pF
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBS16042	
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Package Dimension



Input	1
Output	5
Ground	2,3,4,6

Package: DIP2712

Unit: mm

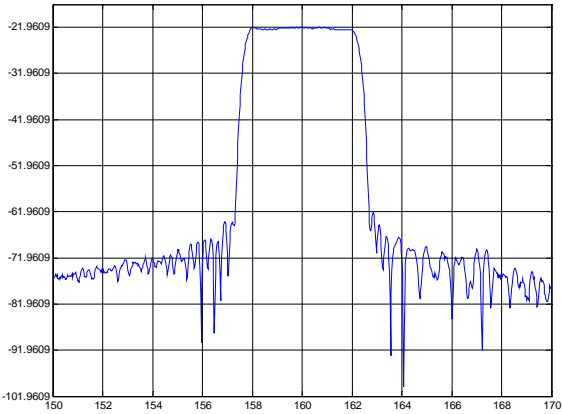


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

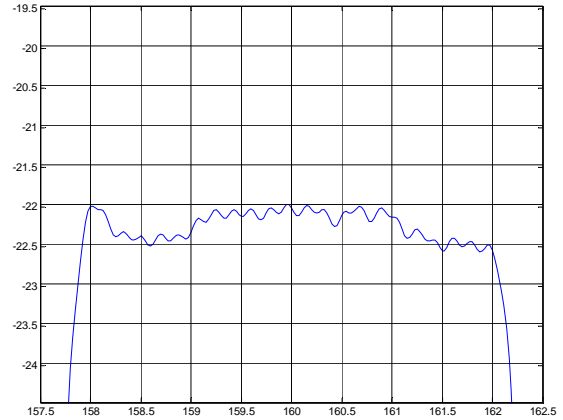
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Typical Performance

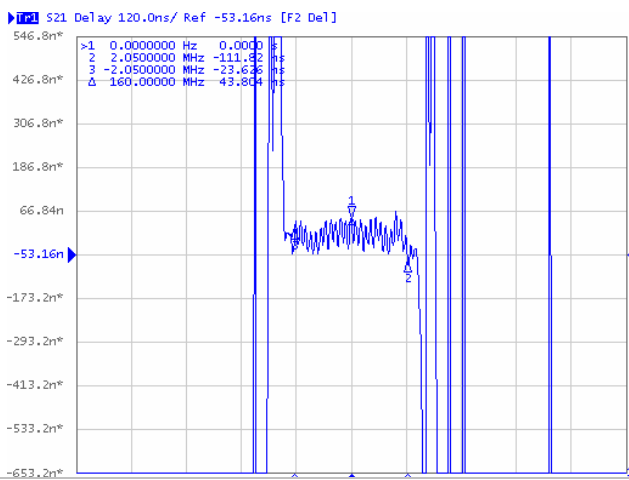
Frequency Respond



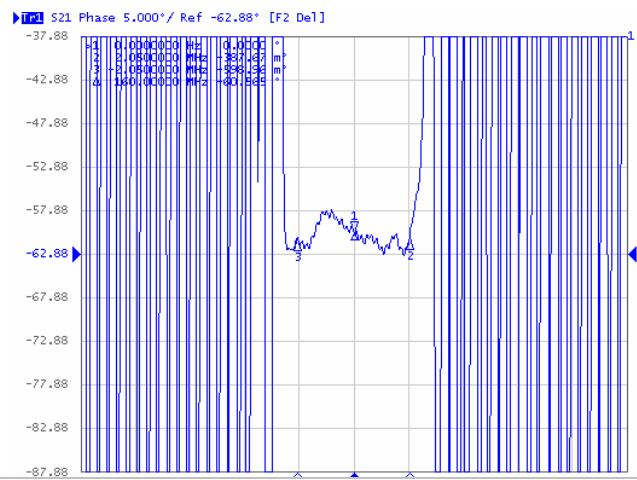
Passband Respond



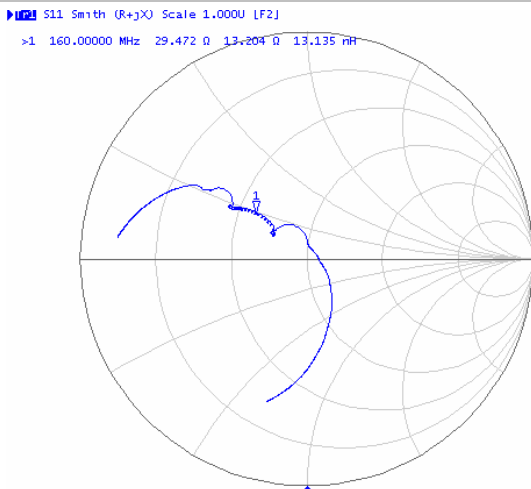
Group Delay Variation($f_0 \pm 2.05\text{MHz}$)



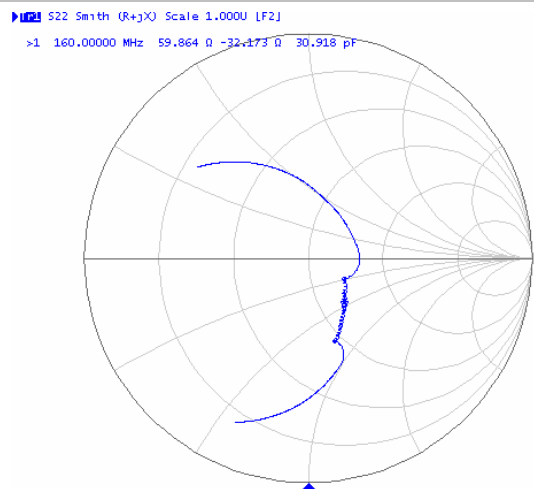
Phase Linearity($f_0 \pm 2.05\text{MHz}$)



Smith Chart S11



Smith Chart S22



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