

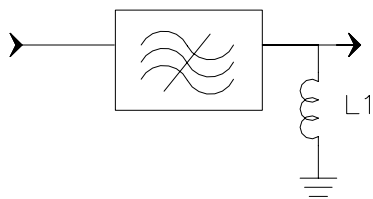
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	159.9	160	160.1
Insertion Loss	dB	-	25	26
3 dB Bandwidth	MHz	10.8	10.9	-
50dB Bandwidth	MHz	-	13.4	13.5
Passband Variation	dB	-	0.4	1
Ultimate Rejection	dB	50	52	-
Absolute delay	usec	-	2.3	2.5
Material Temperature coefficient	KHz/°C	-2.88		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.0x12.8x4.7mm ³)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown


Matching Configuration



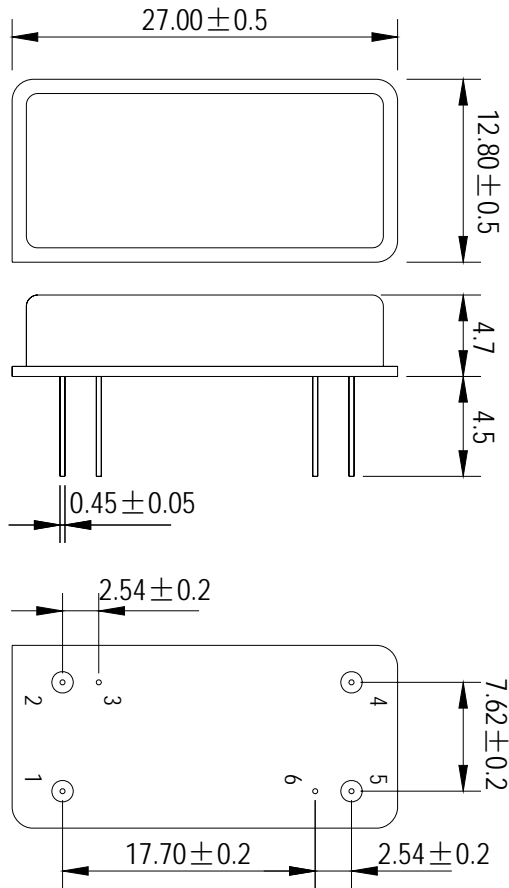
L1 = 22nH

Source/Load Impedance = 50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBT16025	
		Rev. Date	2005-7-12	
		Rev.	1.0	Page

Package Dimension



Input:1
Output:5

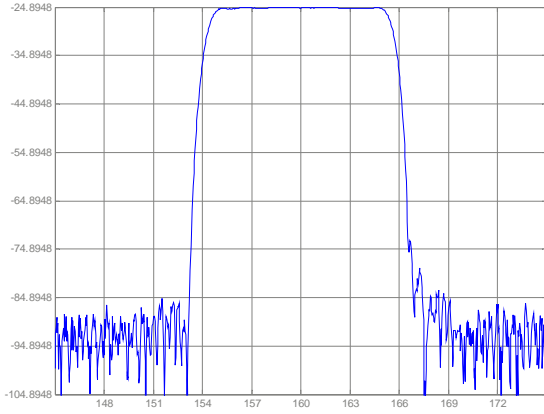


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

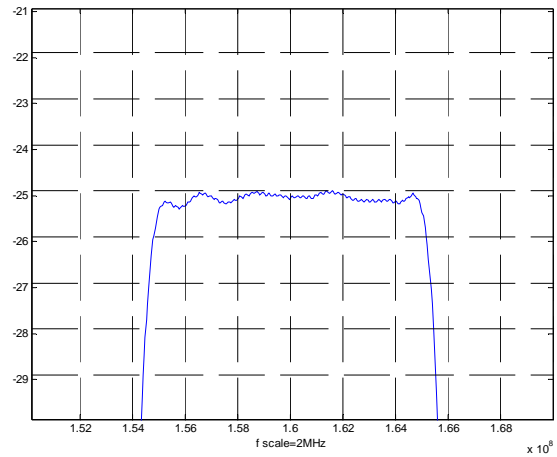
Part Number	LBT16025	
Rev. Date	2005-7-12	
Rev.	1.0	Page 2/3

Typical Performance

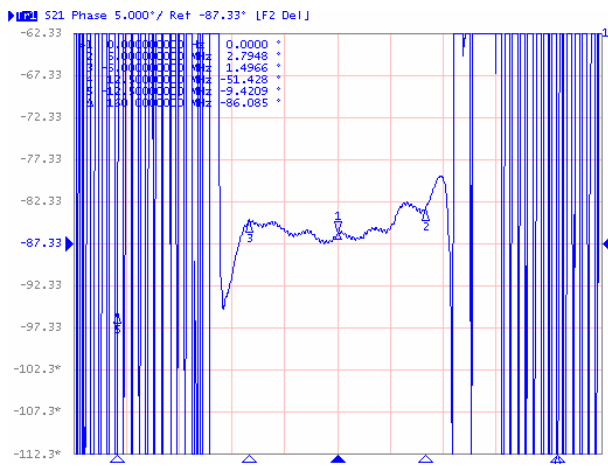
Frequency Response



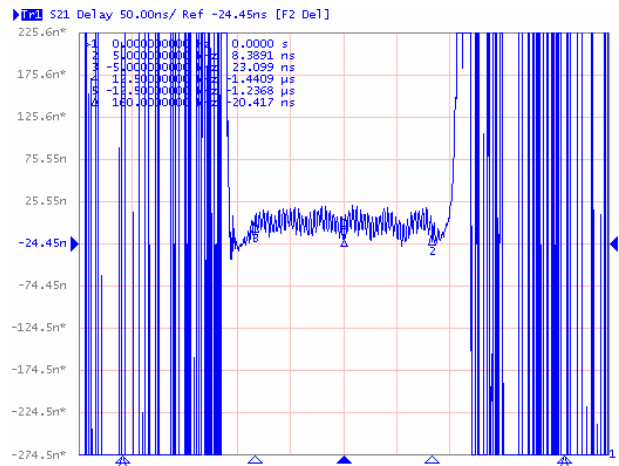
Passband Response



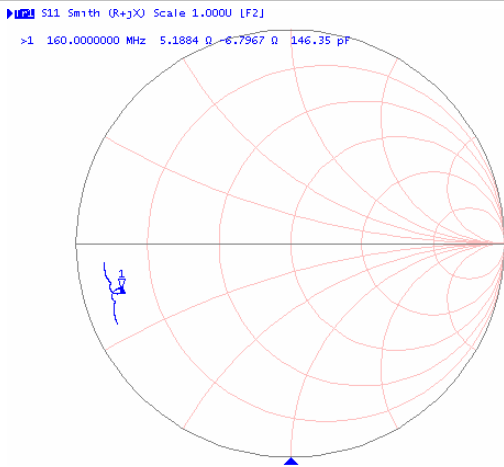
Phase Linearity (f0±5MHz)



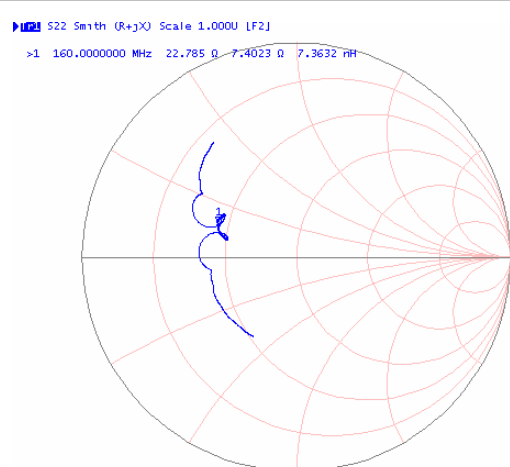
Group delay Variation (f0±5MHz)



Smith Chart S11



Smith Chart S22



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number

LBT16025

Rev. Date

2005-7-12

Rev.

1.0

Page 3/3