

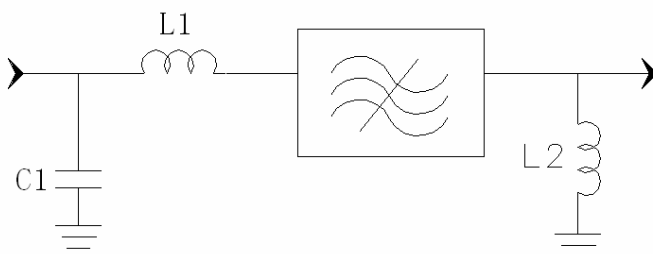
Specifications

| Parameter | Unit | Minimum | Typical | Maximum |
|---|---|---------|---------|---------|
| Center Frequency | MHz | 159.9 | 160 | 160.1 |
| Insertion Loss | dB | - | 30 | 31.5 |
| 1.5 dB Bandwidth | MHz | 13.9 | 14.07 | - |
| 3 dB Bandwidth | MHz | 14.1 | 14.29 | - |
| 40 dB Bandwidth | MHz | - | 15.4 | 15.6 |
| 50 dB Bandwidth | MHz | - | 15.55 | - |
| Passband Variation($f_0 \pm 6.875\text{MHz}$) | dB | - | 0.8 | 1.0 |
| Absolute Delay | usec | - | 3.32 | - |
| Group Delay Variation($f_0 \pm 6.875\text{MHz}$) | nsec | - | 80 | 100 |
| Ultimate Rejection | dB | 45 | 50 | - |
| Material Temperature coefficient | KHz/°C | -2.88 | | |
| Ambient Temperature | °C | 25 | | |
| Package Size | DIP3512 (35.0x12.8x4.7mm ³) | | | |

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=27nH L2=15nH
C1=36pF
Source/Load Impedance=50 ohm

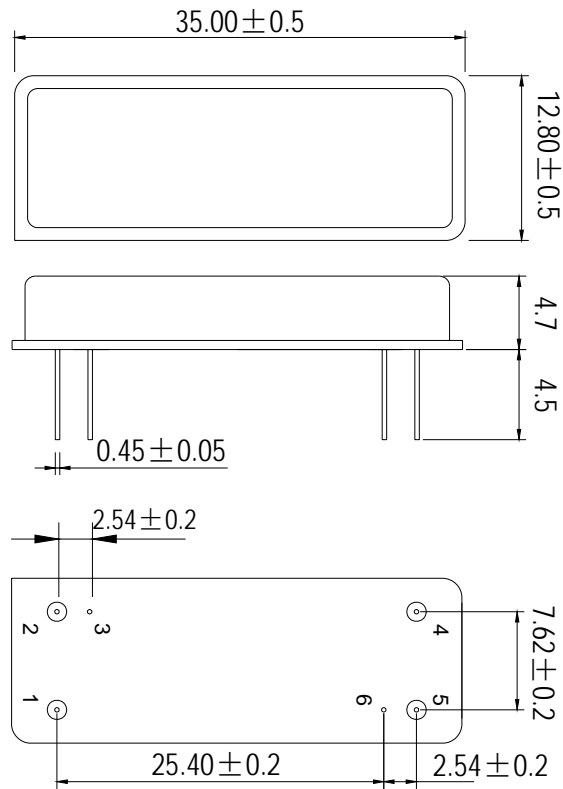
Notes - Component values may change depending on board layout.



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Package Dimension



Input:1
Output:5

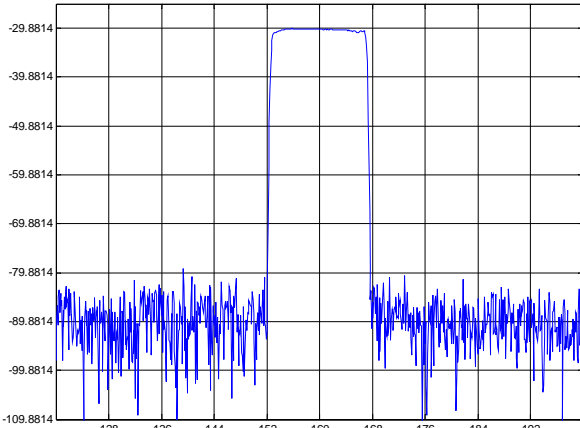


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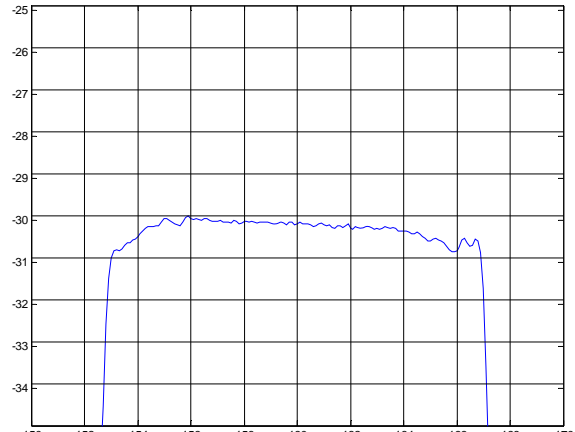
Typical Performance

Frequency Respond



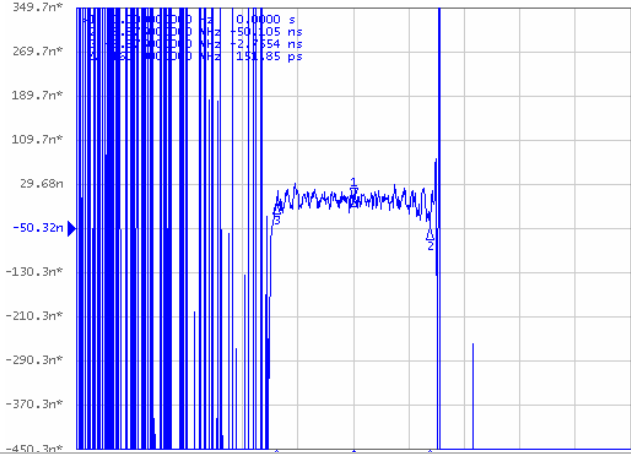
Group Delay Variation($f_0 \pm 6.875\text{MHz}$)

Passband Respond



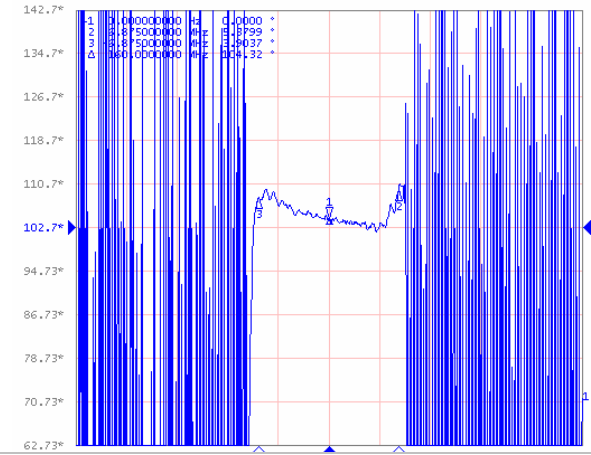
Phase Linearity($f_0 \pm 6.875\text{MHz}$)

S21 Delay 80.00ns/ Ref -50.32ns [Del]



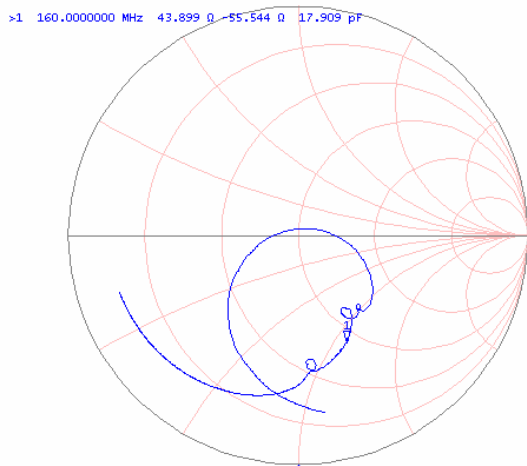
Smith Chart S11

S21 Phase 8.0000/ Ref 102.733 [Del]

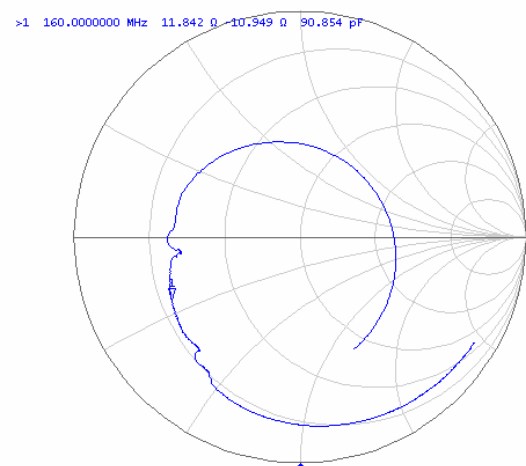


Smith Chart S22

S11 Smith (R+jX) Scale 1.0000



S22 Smith (R+jX) Scale 1.0000



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